

10978 U.S. PTO
10/060569
01/29/02

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PATENT NUMBER and
ISSUE DATE

10597

AUG 26 2003

6610597

U.S. UTILITY Patent Application

APPL NUM 10060569	FILING DATE 01/29/2002	CLASS 438	SUBCLASS 1763	EXAMINER Gouldrean
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APPLICANTS: Kobayashi Migaku;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CON OF 09/640,959 08/16/2000, now abandoned

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 230500/1999 08/17/1999

FG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no
Verified and Acknowledged Examiner's initials		9999 (11-02)
TITLE : Method of fabricating a semiconductor device		

FORMAL DRAWINGS

DEPT. OF COMM./PAT. & TM-PTO-438L (Rev. 12-94)

ISSUE FEE IN FILE

NOTICE OF ALLOWANCE MAILED 03/25/03		CLAIMS ALLOWED	
		Total Claims 27	Print Claim for O.G. /
ISSUE FEE \$165		DRAWING	
Amount Due 1600.00	Date Paid 6-24-03	Sheets Drwg. 12	Figs.Drwg. 34
<input type="checkbox"/> TERMINAL		Print Fig. 8d	
DISCLAIMER		Application Examiner M. Whist	
PREPARED FOR ISSUE			
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